

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
23	BRS	L23	91	(CDR or data adj2 clock\$1 near3 recover\$3 or clock\$3 adj2 data\$1 near3 recover\$4 or data adj2 clock\$1 near3 synchroniz\$8 or clock\$3 adj2 data\$1 near3 synchroniz\$8) same (PLD or programmable near3 circuit\$3 or programmable near3 device\$1 or programmable near3 logic\$1 or DPLD or PDLD)	US- PGPUB ; USPAT ; EPO; JPO	2005/03/1 6 09:52	
24	BRS	L24	1	("6624766").URPN.	USPAT	2005/03/1 6 10:17	
25	BRS	L25	15	("4264973" "4520346" "4910586" "5056118" "5075792" "5349349" "5349588" "5590316" "5614901" "5714904" "5757297" "5805632" "5946355" "6246348" "6346895").PN.	US- PGPUB ; USPAT ; USOCR	2005/03/1 6 10:17	

	Error Definition	Err ors
23		
24		
25		

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
26	BRS	L26	193	(CDR or data adj2 clock\$1 near3 recover\$3 or clock\$3 adj2 data\$1 near3 recover\$4 or data adj2 clock\$1 near3 synchroniz\$8 or clock\$3 adj2 data\$1 near3 synchroniz\$8) same (PLD or programmable near3 circuit\$3 or programmable near3 device\$1 or programmable near3 logic\$1 or DPLD or PDLD or FPGA\$1 or DPLL or PPLL or programmable near3 PLL\$1 or programmable near3 phase adj2 lock\$3 adj2 loop\$1)	US- PGPUB ; USPAT ; EPO; JPO	2005/03/1 6 10:55	
27	BRS	L27	102	26 not 23	US- PGPUB ; USPAT ; EPO; JPO	2005/03/1 6 10:55	

	Error Definition	Err ors
26		
27		